# A Contribution to the Voltage Stability Studies within Power Systems: Some Aspects Related to the Load Representation

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Abstract - This work investigates the impact of the load representation upon a power system security assessment. This is done according to two important aspects: the adopted direction for the load increment and the dynamic models used to represent its behavior. The first aspect is tackled through an evaluation of contingencies, which is based on the system's load flow model for two load increment directions: the "traditional" approach, where the system active and reactive load demands are increased proportionally to the base case, and a second approach that regards the "worst case", in which it is adopted the direction that gives the smallest "local" loading margin for the base case. The second aspect is tackled through a system multi-machine modelling that takes into account the impact of the generators, loads, Automatic Voltage Regulators (AVRs), Over Excitation Limiters (OXLs) and Load Tap Changers (LTCs) dynamic behavior. The dynamic responses of the system are compared to the most critical contingencies previously obtained. Some discrepancies existing in the power system security analysis are analysed for both the increment directions and the dynamic load models through simulations obtained for the IEEE 14 bus system.

*Index Terms*— Voltage stability, Load models, Power system security, Power system dynamic simulation.

#### I. INTRODUCTION

Presently, power systems are facing a restructuring process due mainly to economical and environmental factors. From the economical viewpoint, the new reality imposed to power systems is characterized by a competitive atmosphere, among other factors, originated by the disintegration of the generating capacity. From the environmental viewpoint, the natural resources needed to expand those systems are becoming shorter than some years ago. Moreover, there are social pressures to preserve these resources and so minimise the environmental implications.

As a consequence, the possibility to attend new demand by expanding the power systems (installation of new generating units and transmission lines) is becoming highly jeopardized. Besides, transmission and generation reserves should be lesser than ever in order to minimise production and transmission

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Hernán Prieto Schmidt is with the Electrical Engineering Department, Polytechnic School of the University of São Paulo, Brazil (hernanps@usp.br). costs, guaranteeing the competitiveness among companies and saving the already scarce natural resources. These facts make power systems operate with more reduced stability margins.

So, it is necessary to know the power systems operation limits in order to guarantee quality and safety margins to power system supplying. Among the limits commonly investigated (such as thermal overload limits and angular instability) voltage instability limit is seen as one of the prominent threats to the safe operation of the electric system [1]-[3]. This is the reason why in the last years there have been issued researches [4] on this topic. Moreover, it is known that the dynamic behavior of the load is the key element for a number of incidents related to voltage instability (and collapse) phenomenon observed [3].

This work investigates the impact of some aspects related to the load representation for the voltage stability assessment. From the static viewpoint, it is analysed the impact of adopting different directions for the load increment to the contingency screening and ranking process. From the dynamic viewpoint, it is analysed the impact of adopting different dynamic models to represent the load through a multimachine representation.

#### II. GENERIC REPRESENTATION OF THE POWER SYSTEM

In a generic representation of the power system, the electric network is commonly represented through algebraic equations. On the other hand, short-term dynamics associated to generators, automatic voltage regulators (AVRs), turbines and induction motors are represented through differential equations. A combination of differential and discreet equations varying with time is used to analyse long-term dynamics associated to transformers load tap-changers (LTCs), over-excitation limiters (OXLs) of generators and power recovery characteristic of loads. The non-linear differential-algebraic equations (DAE) can be represented by the following equations [2]:

$$0 = g(x, y, z_c, z_d) \tag{1}$$

$$\dot{x} = f(x, y, z_c, z_d) \tag{2}$$

$$z_{c} = h_{c}(x, y, z_{c}, z_{d})$$
(3)

$$z_{d}(k+1) = h_{d}(x, y, z_{c}, z_{d}(k))$$
(4)

Where, y is the voltage vector in the buses.

x is the state variables vector associated to the short-term dynamics.

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 $z_c$  and  $z_d$  represent the continuous and discreet vectors, respectively; associated to the long-term dynamics.

So, the mathematical model described by (1) through (4) regards the short and long-term dynamics simultaneously. It is known that the time constants characterizing the short and long-term dynamics in a power system can vary from microseconds to minutes, providing a *stiff* characteristic to the DAE [2] which hampers its numeric solution. This difficulty can be tackled using implicit integration methods having *A-stability* properties and also variable-step integration strategies [5].

## III. POWER SYSTEM SECURITY ANALYSIS

There is not yet full agreement on whether voltage stability should be evaluated through a static or a dynamic approach. The former uses a purely algebraic model that describes the power system behavior through (1); its solution is obtained similarly to the approach used to solve load flow. The latter alternative uses the generic model represented in (1)-(4) and requires a relatively bigger computational effort. It also needs an additional engineering work for analyzing the results [3].

So, the power system security analysis is usually accomplished using a combination of the two approaches previously mentioned. The static approach is used to filter the most critical contingencies that can take the system to a voltage instability condition or even to a voltage collapse. The dynamic approach is used to perform a detailed analysis of the most critical contingencies captured by the static approach. Its aim is to identify the actions that need to be adopted in order to improve the system security. The numeric methods used in the screening and ranking of the most critical contingencies as well as in the power system stability dynamic analysis are presented next.

#### IV. EVALUATION OF THE MOST CRITICAL CONTINGENCIES

In order to select and classify the most critical contingencies, we adopt the post-contingency loading margin as severity index (SI). Such an index is calculated from a continuation power flow [6] along to a load increment direction. Next, a brief description of the continuation power flow method and an analysis on the load increment direction impact upon the system's load margin calculation is presented.

## A. Continuation Power Flow [6], [7]

The continuation power flow is based on the system static model (1). It basically calculates the successive equilibrium points for (5) assuming slow variations of the  $\lambda$  parameter which represents the increment in load demand and power supplied by the system generators.

$$0 = g(y, \lambda) \tag{5}$$

Equation (5) can be re-written as load flow expressions:

$$P_{Gi}(\lambda) - P_{Li}(\lambda) = \sum_{j \in i} V_i \cdot V_j \cdot (G_{ij} \cdot \cos \theta_{ij} + B_{ij} \cdot \sin \theta_{ij})$$
(6)

$$Q_{Gi}(\lambda) - Q_{Li}(\lambda) = \sum_{j \in i} V_i \cdot V_j \cdot (G_{ij} \cdot \sin \theta_{ij} - B_{ij} \cdot \cos \theta_{ij})$$
(7)

The increments of the generators active power (8) and the demand in the buses (9) and (10) are given by:

$$P_{Gi}(\lambda) = P_{Gi0} \cdot (1 + \lambda \cdot K_{Gi})$$
(8)

$$P_{Li}(\lambda) = P_{Li0} \cdot (1 + \lambda \cdot K_{PLi}) \tag{9}$$

$$Q_{Li}(\lambda) = Q_{Li0} \cdot (1 + \lambda \cdot K_{QLi})$$
(10)  
Where,

 $P_{Li0}$  and  $Q_{Li0}$  represent the load active and reactive power of the  $i^{th}$  bus, assuming the base case ( $\lambda$ =0).

 $P_{Gi\theta}$  is the active power supplied by the generator of the *i*<sup>th</sup> bus, assuming the base case ( $\lambda$ =0).

 $K_{PLi}$  and  $K_{QLi}$  are coefficients defining the load power factor of the  $i^{th}$  bus.

 $K_{Gi}$  is a coefficient defining the generator's participation factor in the  $i^{th}$  bus for a certain loading level ( $\lambda$ ).

Equations (6)-(7) can be written in a compact form as:

$$g(y,\lambda) = G(y) + \lambda \cdot d = 0 \tag{11}$$

In (11), *d* represents a vector indicating the direction of the active and reactive power increment consumed by the loads and the active power supplied by the generators.

The successive solutions of (5), for different values of the  $\lambda$  parameter, are obtained by the continuation power flow through a predictor-corrector scheme showed in Figure 1.

The predicting stage consists in obtaining, from a given point  $(y_1, \lambda_1)$ , an approximate solution  $(y_2^*, \lambda_1 + \Delta \lambda)$  for the new loading level  $\lambda_2$ . In this work, the tangent vector (12) to the V $\lambda$  curve is being used in the predicting stage. The correcting stage consists in calculating the  $(y_2, \lambda_2)$  point defined by the intersection of the line perpendicular to the tangent vector passing through the  $(y_2^*, \lambda_1 + \Delta \lambda)$  point and intersects the V $\lambda$  curve. Such point is obtained using (13) and (14).



Figure 1. Predictor and Corrector steps of the Continuation Power Flow Method

$$\frac{\partial y}{\partial \lambda}\Big|_{(y_1,\lambda_1)} = -\left(\frac{\partial g}{\partial y}\Big|_{(y_1,\lambda_1)}\right)^{-1} \cdot \frac{\partial g}{\partial \lambda}\Big|_{(y_1,\lambda_1)}$$
(12)

$$0 = g(y, \lambda) \tag{13}$$

$$0 = \Delta \lambda_1 \cdot (\lambda - \lambda_1 - \Delta \lambda_1) + \Delta y_1^T \cdot (y - y_1 - \Delta y_1)$$
(14)

By applying the described methodology, the loading margin  $\lambda_{C}$ - $\lambda_{B}$ , seen in Figure 1, is obtained for each analysed contingency.

### B. Evaluation of the Load Increment Direction

In order to evaluate the impact of the adopted direction for the load increment (evaluation of the system security), it is considered two directions of load increment: one direction, herein referred to as "traditional", where the active and reactive demands increase proportionally to the base case ( $K_{PLi}=K_{QLi}=1$  in (9) and (10)) and another direction, referred to as "worst case" [8]. In the "worst-case", the adopted direction for the active and reactive load increment corresponds to the smallest local loading margin for the case base.

The  $d^*$  direction corresponding to the "worst case" is obtained using the methodology proposed in [8] and [9], briefly described next:

- (i). Select any vector d<sub>i</sub> = d<sub>0</sub> as an initial estimation for the d\* direction corresponding to the "worst case" of load increment, |d<sub>0</sub>|=1;
- (ii). Stress the system by incrementing λ according to the d<sub>i</sub> direction until the Jacobian matrix of the load flow turns singular (see point C in Fig.1);
- (iii). Make  $d_{i+1} = w_i \cdot \partial g(y, \lambda) / \partial \lambda$ ,  $|d_{i+1}| = 1$ ;
- (iv). Repeat steps (ii), (iii) and (iv) until  $d_i$  converges for the value of  $d^*$ .

Where,

 $w_i$  is the left eigenvector with respect to the load flow Jacobian  $\partial g(y, \lambda)/\partial y$ .

Once applied the two methodologies described in Sections IV(A) and IV(B), it is possible to obtain a list of the most severe contingencies for the two load increment directions previously considered. Next, the most severe contingencies for both scenarios are analysed using a multi-machine simulator.

## V. VOLTAGE STABILITY DYNAMIC ANALYSIS

In order to investigate the load dynamic behavior for the most critical contingencies, we developed a multi-machine dynamic simulator based on the network's generic model described in (1)-(4). This simulator is based on the DASSL (*Differential Algebraic System SoLver*) solver [10] which is adequate for the solution of stiff differential-algebraic equations. Next, a description of the DASSL method and the dynamic models of the devices included in such a simulator are presented.

## A. The DASSL Solver [10]

The DASSL solver is suited to solve stiff

differential-algebraic equations having indexes zero and one, such as:

$$F(t, y, \dot{y}) = \begin{cases} \dot{y} = f(y, t) \\ 0 = g(y, t) \end{cases}$$
(15)

$$y(t_0) = y_0 \tag{16}$$

$$\dot{y}(t_0) = \dot{y}_0 \tag{17}$$

Where,

F(t, y, dy/dt) represents the system described in (1)-(4).

y and dy/dt are vectors of order N having the variables described in Section II.

DASSL is a variable step and variable order DAE solver based on the BDF methods (Backward Differentiation Formulae). It presents two steps for solving (15).

In the predictor step it uses a divided difference polynomial (18) in order to interpolate the  $y_{j+1-k}$  solution points of the last *k* time intervals and so obtaining the first approximation of  $y_{j+1}$ .

$$y_{j+1}(t) = y_{j} + (t - t_{j}) \cdot \langle y_{j}, y_{j-1} \rangle + \dots$$

$$+ (t - t_{j}) \cdot (t - t_{j-1}) \cdot (t - t_{j-k+1}) \cdot \langle y_{j}, \dots, y_{j-k} \rangle$$
(18)

Where the divided differences are defined by:

$$\left\langle \boldsymbol{y}_{j}\right\rangle = \boldsymbol{y}_{j} \tag{18}$$

$$\langle y_{j}, \dots, y_{j-k} \rangle = \frac{\langle y_{j}, y_{j-1}, \dots, y_{j-k+1} \rangle - \langle y_{j-1}, y_{j-2}, \dots, y_{j-k} \rangle}{t_{j} - t_{j-k}}$$
 (19)

In the corrector step the dy/dt derivative (15) is estimated using a BDF polynomial (20) with order k, obtaining the non-linear algebraic equations (21).

$$\sum_{r=0}^{k} \alpha_{r} \cdot y_{j+1-r} = h \cdot \beta_{0} \cdot F(y_{j+1}, t_{j+1},)$$
(20)

Where,

 $\alpha_r$  and  $\beta_0$  represent the coefficients that depend on the *k* order selected for the BDF formulae.

*h* is the length of the  $j^{th}$  integration step.

$$F(t_{j+1}, y_{j+1}, \dot{y}_{j+1}) = \begin{cases} y_{j+1} + \sum_{r=0}^{k} \alpha_r \cdot y_{j+1-r} - h \cdot \beta_0 \cdot f(y_{j+1}, t_{j+1}) = 0 \\ g(y_{j+1}, t_{j+1}) = 0 \end{cases}$$
(21)

Equation (21) can be solved using a modified Newton method [10].

The method's order and step selection is based on the local truncation error control. At the end of each integration step h it is verified if the estimative for the local truncation error is within the allowed tolerance. Should it be the case the step is accepted and it is determined, using the same error, if there is margin to increase the integration step. Conversely, the step is rejected and it is determined the length of the necessary step so that the error remains within the allowed tolerance [10].

### B. Device Models Included in the Dynamic Simulator

So far, the dynamic simulator possesses the following models of devices.

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<u>Generators</u>: It is considered only the IEEE 1.1 model, as recommended by [11] for stability studies, neglecting the saturation effect.

$$\dot{E}_{q}^{'} - 1/T_{d0}^{'} \cdot \left(E_{fd} - E_{q}^{'} - \left(x_{d} - x_{d}^{'}\right) \cdot i_{d}\right) = 0$$
(22)

$$\dot{E}_{d}' - 1/T_{q0} \cdot \left( -E_{q}' + \left( x_{q} - x_{q}' \right) \cdot i_{q} \right) = 0$$
(23)

$$E_{q} - E_{q}^{'} + r_{a} \cdot i_{q} + x_{d}^{'} \cdot i_{d} = 0$$
(24)

$$E_{d} - E_{d}' + r_{a} \cdot i_{q} - x_{q}' \cdot i_{q} = 0$$
(25)

$$\dot{\omega} = \frac{\omega_0}{2 \cdot H} \cdot \left( T_m - E_q^{'} \cdot i_q - E_d^{'} \cdot i_d - \left( x_q^{'} - x_d^{'} \right) \cdot i_q \cdot i_d - D \cdot \omega_0 / \omega \right)$$
(26)

$$\dot{\delta} - \omega = 0 \tag{27}$$

Where,

 $E'_{d}$  and  $E'_{q}$  are, respectively, the direct and quadrature axis transient voltages (in per unit).

 $E_d$  and  $E_q$  are, respectively, the direct and quadrature axis voltages (in per unit).

 $E_{fd}$  is the field voltage in per unit.

 $i_d$ ,  $i_q$  are, respectively, the direct and quadrature axis currents (in per unit).

 $T'_{d0}$  and  $T'_{q0}$  are, respectively, the direct and quadrature axis open-circuit transient time constants (in seconds).

 $r_a$ ,  $x_d$  and  $x_q$  are, respectively, the armature resistance and the direct and quadrature axis synchronous reactances (in per unit).  $x'_d$  and  $x'_q$  are, respectively, the direct and quadrature axis transient reactances (in per unit).

 $T_m$  and D are, respectively, the mechanic torque and the damping coefficient (in per unit).

w,  $w_0$  and  $\delta$ , respectively, are the rotor angular frequency (in radians/s), the synchronous reference frame angular frequency (in radians/s) and the load angle (in radians).

<u>AVRs</u>: It considers a *Proportional-Integer* (PI) type controller, as described in (28), with the output  $E_{fd}$  constrained in a range from  $E_{MIN}$  to  $E_{MAX}$  due to the presence of an *anti-windup* limiter [12].

$$\dot{E}_{fd} - K_A / T_A \cdot (V_{ref} - V_G - V_{OXL}) + 1 / T_A \cdot E_{fd} = 0$$
(28)
Where

 $K_A$  and  $T_A$  are, respectively, the AVR gain (in per unit) and the time constant (in seconds).

 $V_{ref}$ ,  $V_{OXL}$  and  $V_G$  are, respectively, the AVR reference voltage, the OXL output and the generator bus voltage (in per unit).

<u>OXLs</u>: It considers the model suggested in [3] and which is represented in Figure 2.



Figure 2. OXL model used

Where,

 $K_1$ ,  $K_2$  and  $K_3$  are settings that determine the OXL inverse time characteristic (in per unit).

 $I_{fd}$  and  $I_{fdNOM}$  are, respectively, the generator field current and the AVR field current setting, respectively. (in per unit).  $V_{OXL}$  is OXL output signal (in per unit). It's constrained in a range from  $A_{MIN}$  to  $A_{MAX}$  due to the presence of an *anti-windup* limiter [12].

<u>LTCs</u>: It considers the continuous model suggested by [2] and described in (29).

$$\dot{r} - \left(V - V_{ref}\right) / T_C = 0 \tag{29}$$
Where

V and  $V_{ref}$  are, respectively, the LTC controlled voltage and reference voltage setting (in per unit).

 $T_C$  is the time constant (in seconds).

*r* is the tap value (in per unit/per unit). It's constrained in a range from  $r_{MIN}$  to  $r_{MAX}$  due to the presence of an *anti-windup* limiter [12].

<u>Loads</u>: Aside of the static ZIP and exponential load models, it is considered the dynamic models proposed by Hill et al [13] and Ihara et al [14].

Hill et al model [13]

$$T_{P} \cdot \dot{P}_{r} + P_{r} = P_{0} \cdot (V/V_{0})^{\alpha_{s}} - P_{0} \cdot (V/V_{0})^{\alpha_{r}}$$
(30)

$$T_{Q} \cdot \dot{Q}_{r} + Q_{r} = Q_{0} \cdot (V/V_{0})^{\beta_{s}} - Q_{0} \cdot (V/V_{0})^{\beta_{r}}$$
(31)

$$P = P_r + P_0 \cdot \left( V / V_0 \right)^{\alpha_T} \tag{32}$$

$$Q = Q_r + Q_0 \cdot (V/V_0)^{\beta_r}$$
(33)  
Where,

V and  $V_0$  are, respectively, the post and pre-contingency load voltages (in per unit).

P,  $P_0 \in P_r$  are, respectively, the total, pre and post-contingency active powers (in per unit).

Q,  $Q_0 \in Q_r$  are, respectively, the total, pre and post-contingency reactive powers (in per unit).

 $T_P$  and  $T_O$  are the recovery time constants (in seconds).

 $\alpha_s$  and  $\beta_s$  are, respectively, the active and reactive steady-state voltage dependencies (in per unit/per unit).

 $\alpha_T$  and  $\beta_T$  are, respectively, the active and reactive transient voltage dependencies(in per unit/per unit).

$$P = \{1 + K_P \cdot (V - 1)\} \cdot (1 - P_{drop}) + P_{dyn} \cdot (G - 1) \cdot V^2$$
(34)

$$Q = \{1 + K_Q \cdot (V - 1)\} \cdot (1 - Q_{drop}) + Q_{dyn} \cdot (B - 1) \cdot V^2$$
(35)

$$\dot{G} = -(1/T_p) \cdot (G \cdot V^2 - 1) \tag{36}$$

$$\dot{B} = -(1/T_Q) \cdot (B \cdot V^2 - 1) \tag{37}$$

Where,

V, P and Q are, respectively, the voltage and the active and reactive powers (in per unit).

 $K_P$  and  $K_Q$  are, respectively, the active and reactive voltage dependencies (in per unit/per unit).

 $T_P$  and  $T_Q$  are, respectively, the recovery time constants (in seconds).

*G*,  $P_{drop}$  and  $P_{dyn}$  are, respectively, the load conductance (in per unit), the percent of disconnected active load (due the

under-voltage protection) and the percent of dynamic active load.

*B*,  $Q_{drop}$  and  $Q_{dyn}$  are, respectively, the load susceptance (in per unit), the percent of disconnected reactive load (due the under-voltage protection) and the percent of dynamic reactive load.

## VI. NUMERICAL RESULTS

The methodologies, described in Sections IV and V, have been examined on the IEEE 14-bus system, which static and dynamic data were obtained, respectively, from [15] and [16].

Figure 3 shows a comparison between the "traditional" and the "worst-case" load increment directions. It can be seen that by incrementing the system load along the "traditional" direction the generators can supply an additional of 112.98 MW. By using the "worst case" it reduces to 75.90 MW. Similarly, Figure 4 shows that these additional reactive power supplies are bounded in, respectively, 118.36 MVAr using the "traditional" direction and 78.46 MVAr using the "worst case" direction.





Table 1 shows the results obtained for the contingency analysis applied to the IEEE 14-bus system using the methodology presented in Section IV(A) and the UWPFLOW program [7]. It can be seen that four out of the five contingencies classified as the severest, are captured by the contingency ranking module. This occurs for the two load increment directions analysed. Additionally, it can be observed that the six contingencies classified as severest for the "worst-case" direction present values of severity index less than the one of the severest contingency captured in the "traditional" direction. This result shows the strong dependency between the adopted load increment direction and the contingency screening and ranking for voltage security assessment.

TABLE 1. CONTINGENCY RANKING FOR THE (A) TRADITIONAL AND (B) WORST-CASE DIRECTIONS (IEEE 14-BUS SYSTEM)

	"Traditional" direction				"Worst-case" direction			
	Contingency	SI			Contingency	SI		
1	T05_06	0.19488		1	T05_06	0.08376		
2	L02_03	0.23615		2	L01_05	0.15538		
3	L01_05	0.27734		3	L07_09	0.15872		
4	L07_09	0.29764		4	L09_14	0.18402		
5	T04_07	0.37272		5	L02_03	0.18694		
6	L01_02	0.37704		6	T04_07	0.19413		
7	L02_04	0.3989		7	L06_13	0.20895		
8	L09_14	0.41708		8	L02_04	0.21213		
9	L04_05	0.42282		9	L09_10	0.21439		
10	L06_13	0.42529		10	L01_02	0.21673		
11	T04_09	0.43266		11	L02_05	0.22497		
12	L02_05	0.43565		12	T04_09	0.22708		
13	L09_10	0.46697		13	L06_12	0.23825		
14	L13_14	0.47818		14	L04_05	0.24339		
15	L06_11	0.47847		15	L03_04	0.25072		
16	L06_12	0.48564		16	L06_11	0.25213		
17	L12_13	0.49196		17	L13_14	0.25589		
18	L03_04	0.49489		18	L10_11	0.26161		
19	L10_11	0.49517		19	L12_13	0.26258		

In order to investigate the impact of load increment direction on load recovery mechanism we perform simulations of some contingencies in the IEEE 14 bus system using the dynamic load model proposed in [13] in buses 13 and 14. Table 2 shows the parameters used in load buses 13 and 14.

TABLE 2. PARAMETERS USED IN THE LOAD MODELS OF BUSES 13 AND 14 (IEEE 14-BUS SYSTEM)

Bus	$\alpha_{\rm S}$	$\alpha_{T}$	$\beta_{S}$	$\beta_{T}$	$T_{P}[s]$	$T_Q[s]$
13	-0.32	1.65	-0.48	2.22	70	78
14	-0.16	1.31	-0.77	2.08	61	88

Figures 5 and 6 shows, respectively, the time evolution of bus 13 voltage, load active and reactive power consumption during the contingency L06\_13 for a load consumption increment of 30% above the base case along the "traditional" and the "worst-case" directions.



--- |V\_Bus13| [pu] (base) --- |V\_Bus13| [pu](traditional) --- |V\_Bus13| [pu](worst) Figure 5. Bus voltages at Bus 3 for "base case", "traditional" and "worst-case" increment directions



"traditional" and "worst-case" increment directions

Similarly, Figs 7 and 8 shows, respectively, the time evolution of bus 14 voltage, load active and reactive power consumption during the contingency L06\_13 for a load consumption increment of 30% above the base case along the "traditional" and the "worst-case" directions.









Figures 5 and 7 shows the large differences among voltage time evolution of buses 13 and 14 only by varying the load increment direction used in voltage stability dynamic analysis. Cleary, for both buses, the "worst-case" direction is the severest among the three adopted directions.

Figures 6 and 8 shows the time evolution of load buses 13 and 14 active power responses. Cleary the load active power recovery is more severe in the "worst-case" direction.

#### VII. CONCLUSIONS AND FUTURE WORK

We have examined the impact of load representation upon a power system security assessment. Two important aspects are investigated: the adopted direction for load increment and the dynamic load models used to represent its behavior. These two aspects are evaluated by using: a method to determine the "traditional" and "worst-case" load increment directions [8], [9]; a continuation power flow algorithm [7] for contingency screening and ranking; and a DASSL [10] based power system simulator to perform the dynamic analysis of the most critical contingencies. The results obtained for the IEEE 14 bus system show the high impact of the analysed aspects in power system security assessment.

A similar investigation for the Ihara[14] dynamic load model and an extension of this work for the IEEE 30 and 57-bus systems are currently underway.

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