Impact of Temperature and Doping Concentration on Avalanche Photodiode Characteristics

T. Bendib, L. Pancheri, F. Djeffal and G.-F. Dalla Betta

Abstract—In this paper, the effect of temperature and doping concentration on the breakdown voltage, multiplication gain and noise factor of abrupt junction avalanche photodiodes has been investigated for hole- and electron-initiated processes. Temperature-dependent models have been developed including the temperature variation of impact ionization coefficients, bandgap, intrinsic carrier concentration, dielectric constant and built-in-potential. The developed models have been used to study the impact of temperature and doping concentration on the device characteristics. The models can be implemented in simulation tools, in order to study avalanche photodiode-based systems including thermal effects.

Index Terms—Avalanche photodiode, multiplication gain, noise factor, breakdown voltage, temperature sensitivity.

I. INTRODUCTION

A valanche photodiodes (APDs) are widely used in several applications such as instrumentation, nuclear medicine and aerospace, thanks to their high speed and high sensitivity [1-2]. The increase in photodetectors market is fostering the development of new designs based on the optimization of cost/efficiency ratio for larger scale mass production. In CMOS technology era, the avalanche photodiode is considered as promising candidate to meet the need for optical sensors with improved speed and detection efficiency. This is mainly due to the low manufacturing costs of the silicon APDs, their compatibility with CMOS monolithic integration, and their excellent optical properties.

On the other hand, the integration of efficient low-noise photodiodes in standard CMOS technology requires the introduction of custom doping implantation, which is necessary to improve temperature stability, reduce dark current and excess noise. Therefore, in order to get a global view of avalanche photodiode performance, new models, designs and optimization approaches are necessary for the comprehension of the fundamentals device characteristics. In this context, several studies have been proposed to investigate the avalanche photodiode and to gain a better

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understanding of its behavior [3-5].

Several analytical/experimental works have been recently published to study the impact of doping level on the performance of APD [6–7]. In addition, there are many research papers available on the temperature effects on APDs [9–10]. However, it is missing a careful analysis of the impact of doping concentration on the temperature sensitivity of APD performance, which has been presented in this paper. Temperature-dependent models have been incorporated for all the relevant physical quantities: impact ionization coefficients, bandgap, intrinsic carrier concentration, dielectric constant and built-in-potential.

II. MODEL FORMULATION

Based on McIntyre theory and a one-dimensional analysis, the multiplication gain M and the excess noise factor F for carriers generated at x are expressed as [11]:

$$M(x) = \frac{\exp\left(-\int_{x}^{W} (\alpha - \beta) dx\right)}{1 - \int_{0}^{W} \alpha \exp\left(\int_{x}^{W} (\alpha - \beta) dx\right) dx'}$$
(1)
$$F(x) = 2 + \frac{M(W) \left[2\int_{0}^{W} \alpha M^{2}(x) dx - 1\right]}{M(x)}$$
(2)

where α and β are the electron and hole impact ionization coefficients and *W* is the space-charge region width. α and β are dependent on temperature and applied electric field and expressed by the Okuto-Crowell model as [12,13]:

$$\alpha = A_N(T) \exp\left(-\frac{B_N(T)}{E}\right)^{\delta_n}$$
(3)

$$\beta = A_P(T) \exp\left(-\frac{B_P(T)}{E}\right)^{\delta_P}$$
(4)

where *T* is absolute temperature and *E* is the electric field. The parameters A_N , A_P , B_N and B_P are a function of temperature used to parameterize the temperature dependence of α and β , as well as the electric field dependence. A_N , A_P , B_N and B_P are expressed as [12,13]

$$A_{N,P}(T) = a_{n,p}(1 + c_{n,p}(T - 300))E^{\gamma_{n,p}}$$
(5)

$$B_{N,P}(T) = b_{n,p}(1 + d_{n,p}(T - 300))$$
(6)

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These models are suitable for bulk ionization coefficients over the electric field range of $10^5 - 10^6 \text{ V} \cdot \text{cm}^{-1}$ and the temperature range of 15°K-500 °K [12].

The breakdown voltage, that is theoretically defined as the voltage when the multiplication gain approaches infinity, can be calculated as an extrapolation of (1).

In order to study the impact of doping concentration on the temperature sensitivity of the device, temperature dependency of various semiconductor parameters like bandgap (E_{p}) , intrinsic carrier concentration (n_{i}) , dielectric constant of silicon (ε_{si}) and built-in-potential (V_{bi}) have been implemented, as given below [13]:

$$N_{c}(T) = N_{c}(300) \times \left(\frac{T}{300}\right)^{\frac{2}{3}}$$
(7)

$$N_V(T) = N_V(300) \times \left(\frac{T}{300}\right)^{\overline{3}}$$
 (8)

$$E_{g}(T) = E_{g}(300) + E_{g\alpha} \left(\frac{300^{2}}{300 + E_{g\beta}} - \frac{T^{2}}{T + E_{g\beta}} \right)$$
(9)

$$n_i(T) = \sqrt{N_c(T)N_v(T)} \exp\left(\frac{-qE_g(T)}{2kT}\right)$$
(10)

$$V_{bi}(T) = \frac{kT}{q} \log\left(\frac{N_A N_B}{n_i^2(T)}\right)$$
(11)

$$\varepsilon_{si}(T) = 11.4 \times (1 + 1.3 \times 10^{-4} T) \times \varepsilon_0$$
(12)
where:

$$N_{C}(300) = 2.8 \times 10^{19} \, cm^{-3} \, N_{V}(300) = 1.04 \times 10^{19} \, cm^{-3} \, .$$

 $E_g(300) = 1.08 eV$, $E_{g\alpha} = 4.73 \times 10^{-4} eV/K$, $E_{g\beta} = 636K$, These parameters are used to calculate the electric field distribution in abrupt junction n+/p, which is given by:

$$E(x) = E_M(T) \left(1 - \frac{x}{W(T)} \right)$$
(13)

where
$$E_M = \left(\frac{qN_BW(T)}{\varepsilon_{si}}\right)$$
 (14)

$$W(T) = \sqrt{\frac{2\varepsilon_{si}(T)(N_A + N_B)}{qN_A N_B}} (V_{bi}(T) + V)$$
(15)

III. RESULTS AND DISCUSSION

In this section, the breakdown voltage, the multiplication gain and the excess noise factor are calculated using Eqs. (1,2) in order to evaluate the APD performances at different temperature and substrate doping levels. The electron and hole initiated multiplication gain obtained with our combined model, predicted for different temperatures between $T = 15^{\circ}K$ and 500°K, are illustrated in Figure 1. As expected, this figure shows that, since electron ionization coefficients are larger than hole coefficients in Si devices, the electron-initiated multiplication gain is larger than that the hole initiated process. However, this difference reduces multiplication by increasing the region doping concentration, as a result of the reduction of ionization coefficient ratio at high electric fields.



Fig. 1. Electron and hole initiated gain versus bias voltage for APD at various temperatures in the range of $15^{\circ}K < T < 500^{\circ}K$ (a) and (b) with NB= 5×10^{14} cm⁻³, (c) and (d) with NB= 2×10^{17} cm⁻³. The electron and hole initiated processes are designated by e and h, respectively.

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Moreover, the ratio between the electron and hole initiated characteristics is larger as the bias voltage approaches the breakdown value for both low and high doping levels. This ratio, close to breakdown voltage, becomes smaller at high temperatures for low substrate doping level (Figure2.a) and almost saturates at the value of approximately 3.2 for high substrate doping level (Figure2.b).



Fig. 2. Ratio of electron and hole initiated gain versus bias voltage for APD at various temperatures in the range of $15^{\circ}K < T < 500^{\circ}K$ (a) with NB=5×10¹⁴cm⁻³, (b) with NB=2×10¹⁷cm⁻³.

Fig. 3 shows the excess noise factor versus multiplication gain for both electron and hole initiated process calculated for the investigated APDs with different doping concentration, at three temperature values 15° K, 300° K and 500° K.

This figure illustrates that the increase in the ambient temperature reduces the excess noise for a given doping concentration N_B in the case of pure electron injection, while F increases in the case of pure hole injection. This behavior results from a decrease of ionization rates ratio at increasing temperatures. Furthermore, as can be observed from Figure 3, at a concentration of $N_B=2\times10^{17}$ cm⁻³, the electron and hole initiated processes are less sensitive to the temperature variations, and also their corresponding excess noise factor. As a result APDs with larger multiplication doping levels are more stable with respect to ambient temperature variations.



Fig. 3. Excess noise factor *F* as a function of the electron and hole multiplication gain at various temperatures in the range of 15° K < T < 500°K with N_B=5×10¹⁴cm⁻³ and N_B=2×10¹⁷cm⁻³. The electron and hole initiated processes are designated by *e* and *h*, respectively.

Figure 4 shows the temperature dependence of breakdown voltage for doping concentration range of 5×10^{14} cm⁻³ $< N_B < 2 \times 10^{17}$ cm⁻³.

This figure demonstrates that an increase in ambient temperature causes the breakdown voltage to increase for a given substrate doping concentration. The obtained behavior is due to an increase of the phonon population with temperature. Therefore, a higher electric field and applied voltage are required to reach breakdown in order to overcome the increased carrier cooling caused by phonon scattering[14]. It can be observed that the temperature coefficient is reduced by increasing the doping concentration, thanks to the reduction of the multiplication region width [15]. As shown in Figure 4, an approximately linear temperature dependence is found for all doping concentrations in the considered range, where the breakdown voltage variation is strongly reduced as a function of temperature for high doing concentration values. At the highest concentration values considered in Figure 4, it is to note that the breakdown voltage saturates at low temperatures, probably because of the corresponding saturation in the phonon population. On the contrary, the impact of temperature on breakdown voltage value is larger for low doping concentrations. Therefore, the device immunity against the temperature variation is improved by increasing the intrinsic region doping. However, the high doping values can degrade other device characteristics like multiplication gain and excess noise factor, especially electron-initiated process.

The impact of temperature on the noise factor, with various doping levels 5×10^{14} cm⁻³, 1×10^{16} cm⁻³ and 2×10^{17} cm⁻³ was evaluated by extracting the noise factor values at a fixed multiplication gain M=10 for both hole and electron pure injection. As shown in Figure 5, a linear temperature dependence is found for both pure electron and pure hole injection for low and high doping concentration levels.

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Fig. 4. APD avalanche breakdown voltage V_{bd} normalized to the 300°K value as a function of temperature obtained by our combined model with various doping levels.

As noticed in Figure 5, for all the substrate doping concentrations, excess noise factor increases slightly with temperature for pure electron injection and decreases for pure hole injection. Moreover, at an operating temperature, the noise factor is reduced (increased) by increasing of doping level in the multiplication region for pure hole injection (pure electron injection) respectively.



Fig. 5. Excess noise factor F at a gain M=10 as a function of temperature. Simulation obtained by our combined model for pure electron and pure hole injection with various doping levels. The electron and hole initiated processes are designated by e and h, respectively.

IV. CONCLUSION

In this paper, a temperature-dependent model for the simulation of breakdown voltage, multiplication gain and noise factor in silicon APDs is presented. It is found that increasing of doping level in the APD multiplication region reduces the temperature coefficient of gain and noise. Moreover, the temperature sensitivity of breakdown voltage is more pronounced for low doping concentration. It is to note that this investigation can be extended to 2D and 3D simulation domains and applied to different semiconductor materials (for example: SiGe). As local field models are known to over-estimate the APD excess noise factor [11], the value of F provided by the proposed model can be considered as a worst-case estimation. Nonlocal models

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REFERENCES

- M. Moszyński, M. Szawlowski, M. Kapusta and M. Balcerzyk, "Large area avalanche photodiodes in scintillation and X-rays detection," Nucl. Instr. Meth. Phys. Res. A, Vol. 485, pp. 504-521, 2002.
- [2] A. Biber, P. Seitz and H. Jäckel, "Avalanche photodiode image sensor in standard BiCMOS technology," IEEE Trans. Electron Devices, Vol. 47, pp. 2241–2243, 2000.
- [3] A. Rochas, M.Gösch, A. Serov, P.A. Besse, R.S. Popovic, T. Lasser and R. Rigler, "Low-Noise Silicon Avalanche Photodiodes Fabricated in Conventional CMOS Technologies," IEEE Trans. Electron Devices, Vol. 49, pp. 387-394, 2002.
- [4] L. Pancheri, M. Scandiuzzo, D. Stoppa and G.-F. DallaBetta, "Low-Noise Avalanche Photodiode in Standard 0.35-μm CMOS Technology," IEEE Trans. Electron Devices, Vol. 55, pp. 457-461, 2008.
- [5] G.M. Williams, M. Compton, D.A. Ramirez, M.M. Hayat, A.S. Huntington, "Multi-Gain-Stage InGaAs Avalanche Photodiode With Enhanced Gain and Reduced Excess Noise," IEEE Trans. Electron Devices, Vol. 1, pp. 54 - 65, 2013.
- [6] M. Kanishka and D. N. Ranjan, "Gain and bandwidth analysis and comparison for gallium arsenide and silicon avalanche photodiodes with very thin multiplication layer," Optical Engineering, Vol. 52, pp. 54001, 2013.
- [7] L. Pancheri, T. Bendib, G.-F. Dalla Betta and D. Stoppa, "Design and characterization of Avalanche Photodiodes in submicron CMOS technologies," Proc. SPIE 8982 (Optical Components and Materials XI), 898211, 2014.
- [8] K. Ogasawara, S. Livi and D.J. McComas, "Temperature dependence of the thin dead layer avalanche photodiode for low energy electron measurements," Nucl. Instr. Meth. Phys. Res. A, Vol. 611, pp. 93–98, 2009.
- [9] Y-S. Chung, Il-Y. Park, Y-Ik. Choi, S-K. Chung, "Temperature dependent effective ionization coefficient for Si," Microelectronic Engineering, Vol. 51–52, pp. 535–540, 2000.
- [10] S. Masudy-Panah, M.K. Moravvej-Farshi and M. Jalali, "Temperature dependent characteristics of submicron GaAs avalanche photodiodes obtained by a nonlocal analysis," Optics Communications, Vol. 282, pp. 3630–3636, 2009.
- [11] R.J. McIntyre, "A new look at impact ionization-Part I: A theory of gain, noise, breakdown probability, and frequency response," IEEE Trans. Electron Devices, Vol. 46, pp. 1623-1631, 1999.
- [12] Y. Okuto and C.R. Crowell, "Threshold energy effect on avalanche breakdown voltage in semiconductor junctions," Solid-State Electronics, Vol. 18, pp. 161-168, 1975.
- [13] Atlas user's manual, SILVACO TCAD, 2008.
- [14] D. J. Massey, J. P. R. David, and G. J. Rees, "Temperature dependence of impact ionization in submicrometer silicon devices," IEEE Trans. Electron. Devices, Vol. 53, pp. 2328-2334, 2006.
- [15] P. Agarwal, M. J. Goossens, V. Zieren, E. Aksen, and J. W. Slotboom, "Impact ionization in thin silicon diodes," IEEE Electron Device Letters, Vol. 25, no. 12, pp. 807–809, 2004.