Online Harmonic Elimination of SVPWM for Three Phase Inverter and a Systematic Method for Practical Implementation

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Abstract--Pulse Width Modulation (PWM) inverters play a major role in the field of power electronics. Space Vector Modulated PWM (SVPWM) is the popular PWM method and possibly the best among all the PWM techniques as it generates higher voltages with low total harmonic distortion and works very well with field oriented (vector control) schemes for motor control. High quality output spectra can be obtained by eliminating several low order harmonics by adopting a suitable harmonic elimination technique. In this paper, a mathematical model of space vector modulated three phase inverter is formulated and an offline method for generation of Harmonic Eliminated PWM (HEPWM) by eliminating the low order harmonics of the output waveform is prepared. A systematic online method based on Curve Fitting Technique (CFT) and modification of SVPWM output waveform to a harmonic practical eliminated waveform is developed for implementation. The harmonic spectrum analysis of the proposed model is carried out to ensure the effectiveness. The correctness of the proposed online method is verified by comparing the simulation results with the offline method.

Index Terms-- Space Vector Modulated Pulse Width Modulation, Harmonic Eliminated Pulse Width Modulation, Curve Fitting Technique, Modulation Index, Weighted Total Harmonic Distortion.

I. INTRODUCTION

Inverters are commonly employed for Variable Speed AC Drives (VSD), Uninterruptible Power Supplies (UPS), Static Frequency Changers (SFC) etc. Among these, VSD continues to be the fastest growing applications of inverters. An inverter used for this purpose should have the capability of varying both voltage and frequency in accordance with speed and other control requirements.

Voltage Source Inverters are generally classified into two types viz, square-wave and pulse width modulated. These inverters are introduced in early 1960s when forcecommutation technique is developed. The major disadvantage of this inverter is that, for low or medium power applications, the output voltage contains lower order harmonics. This type of drives has been largely superseded by Pulse Width Modulation (PWM) drives and there have been a number of clear trends in the development of PWM concepts and strategies since 1970s [1]. In the mid of 1980s, a form of PWM called Space Vector Modulation (SVM) was proposed which claimed to offer significant advantages over natural and regular sampled PWM in terms of performance, ease of implementation and maximum transfer ratio [2].

The classic SVPWM strategy, first proposed by Holtz [3, 4] and Van der Broeck [5], is now very popular due to its simplicity and good operating characteristics, leads to generation of specific sequence of states of the inverter. In this method, the converter is treated as a single unit, which can assume a finite number of states depending on the combination of states of its controlled switches. In a twolevel converter, combinations result in six active (non-zero) states and two zero states. In digital implementation of the SVM, aim is to find an appropriate combination of active and zero vectors so that the reference space vector (which represents the reference voltage) is best approximated [6]. This technique is also applied to three-level inverters and expanded to the multiple levels [7, 8]. The application of SVPWM to a variable speed electric drive is applied in [9], and a switching sequence is proposed for a multilevel multiphase converter such that it minimizes the number of switching.

One of the major issues faced in PWM schemes is the reduction of harmonic content in the inverter voltage waveforms [10]. As a result of intensive investigations in this area during the past three decades, several methods, such as Newton-Raphson iteration method [11], methods based on symmetric polynomial and resultant theory [12,16] and methods based on genetic algorithm[13] have been proposed to achieve the optimal switching strategy. The commonly used technique is the Selective Harmonic Elimination (SHE) method at fundamental frequency, for which transcendental equations characterizing harmonics are solved by Newton-Raphson method to compute optimal switching angles. However, optimal switching strategies based on the above method is very difficult to solve on-line in real-time control, since the mathematical model consists of a set of non linear transcendental equations at various modulation depths and therefore have multiple solutions in the convergence. The solutions for the above methods can only be done by a computer offline as the calculations are time consuming and difficult to achieve solution by a microprocessor or a Digital Signal Processor (DSP) in real time. Solution results obtained offline are stored in lookup table using a microprocessor or a DSP. The online implementation is then achieved by using the lookup tables and interpolation technique [14, 15].

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The difficulties in achieving optimal switching strategies in real-time is widely recognized and considerable research efforts have been investigated to overcome the previous drawback. One of the early attempts and also the most prominent of such work was published by Taufiq et al. [16]. They derived a set of non-transcendental equations for near optimal solution using sine wave approximation approach which enables on-line computation using digital methods. Other schemes for regular sampled PWM and Space Vector Modulation were suggested by Bowes [17, 18]. The method based on resultant theory for the solution of switching angles was proposed by Chiasson, et al. [19]. Another possible way is to replace approximately each angular diagram by several corresponding straight lines and solution of non-linear equations can be avoided [20]. The piece-wise linear representation of HEPWM switching angles can also be utilized to formulate the online linear equations [21]. In [22], a real-time algorithm for calculating switching angles is proposed which minimizes total harmonic distortion for step modulation. Recently, Curve Fitting Technique (CFT) is adopted for the on-line solution of optimal switching strategy for single phase PWM inverters [23, 24, 25].

This paper offers a mathematical model for SVM three phase inverter and a novel method for the online elimination of the low order harmonics using CFT method. The paper is organized as follows. Section II provides a brief review of mathematical model of SVPWM. Section III presents a brief description of the offline formulation for optimal PWM strategy. Section IV describes the curve fitting technique and development of online equations. Section V provides MATLAB/ Simulink model of space vector modulated three phase inverter. Section VI explicates harmonic performance analysis technique. In Section VII, the developed model is evaluated and the online algorithm is verified by analyzing the harmonic performance.

II. MATHEMATICAL MODEL OF THREE PHASE INVERTER BASED ON SVM

Any three time varying quantities, which always sum to zero and are spatially separated by 120° can be expressed as space vector. As time increases, the angle of the space vector increases, causing the vector to rotate with frequency equal to the frequency of the sinusoids. A three phase system defined by $V_a(t)$, $V_b(t)$, $V_c(t)$) can be represented uniquely by a rotating vector,

 $V = V_{a}(t) + V_{b}(t)e^{j2\pi/3} + V_{a}(t)e^{-j2\pi/3}$

where,

$$V_{a}(t) = V_{m} \sin\omega t$$

$$V_{b}(t) = V_{m} \sin(\omega t - 2\pi/3)$$

$$V_{c}(t) = V_{m} \sin(\omega t + 2\pi/3)$$

In space vector pulse width modulation, three phase stationary reference frame voltages for each inverter switching state are mapped to the complex two phase orthogonal α - β plane. The mathematical transform for converting the stationary three phase parameters to the orthogonal plane is known as the Clark's transformation. The reference voltage is represented as a vector in this plane. In a three-phase system, the vectorial representation is achieved by the transformation given in Fig. 1.



Fig. 1. Relationship between stationary reference frame and α - β reference frame

where, (V_{α}, V_{β}) are forming an orthogonal two phase system as $V = V_{\alpha} + jV_{\beta}$. Fig. 2 shows a typical two-level three phase Voltage Source Inverter (VSI) configuration. In the three phase system, each pole voltage node can apply a voltage between $+V_{DC}/2$ and $-V_{DC}/2$.

The principle of SVM is based on the fact that there are only eight possible switching combinations for a three phase inverter and the basic inverter switching states are shown in Fig. 3.

The vector identification uses a '0' to represent the negative phase voltage level and '1' to represent the positive phase voltage level. Six non-zero vectors (V₁ and V₆) shape the axis of hexagonal and the angle between any adjacent two non-zero vectors is 60° [26]. Two of these states (V₀ and V₇) correspond to a short circuit on the output, while the other six can be considered to form stationary vectors in the α - β complex plane as shown in Fig. 4. The eight vectors are called the basic space vectors.



Fig. 2. Three phase voltage source inverter configuration



Fig. 3. Inverter switching topology

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(1)



Fig. 4. Space vector hexagon

Each stationary vector corresponds to a particular fundamental angular position as shown in Fig. 5.

An arbitrary target output voltage vector, V_{ref} is formed by the summation of a number of these space vectors within



Fig.5 Inverter phasor angular positions in fundamental cycle

one switching period, which is shown in Fig. 6 for a target phasor in the first 60° segment of the plane.

Any space vector lies in the hexagon can be constructed by time averaging of the adjacent two active space vectors and zero vectors. For each switching period T_s ,



Fig. 6. Reference vector as a geometric summation of two nearest space vectors

the geometric summation can be expressed mathematically as,

$$V_{ref} = V_{ref} \angle \theta = \frac{T_1}{T_s} V_1 + \frac{T_2}{T_s} V_2 + \frac{T_0}{T_s} V_0$$
(3)

where, T_1 is the time for which space vector V_1 is selected and T_2 is the time for which space vector V_2 is selected. The block diagram for generating SVM pulses is shown in Fig.7.

SVM can be implemented through the following steps:

1. Computation of reference voltage and angle (θ)

The space vector, V_{ref} is normally represented in complex plane and the magnitude as,

$$|V_{ref}| = \sqrt{V_{\alpha}^{2} + V_{\beta}^{2}}$$

$$\theta = \tan^{-1} \frac{V_{\beta}}{V_{\alpha}}$$

$$V_{\alpha} = V_{ref} \cos \theta$$

$$V_{\beta} = V_{ref} \sin \theta$$

(4)

2. Identification of sector number

The six active vectors are of equal magnitude and are mutually phase displaced by $\pi/3$. The general expression can be represented by,

$$V_n = V_{DC} \cdot e^{j(n-1)\pi/3}, n = 1, 2, \dots, 6$$
(5)

3. Computation of space vector duty cycle

The duty cycle computation is done for each triangular sector formed by two state vectors. The individual duty cycles for each sector boundary state vector and the zero state vector are given by,



Fig. 7. Block diagram for SVM pulse generation

$$\int_{0}^{T_{1}} V_{ref} dt = \int_{0}^{T_{1}} V_{1} dt + \int_{T_{1}}^{T_{2}} V_{2} dt + \int_{T_{2}}^{T_{2}} V_{0} dt$$
(6)

$$d_{\alpha} = \frac{V_{ref}}{V_{DC}} \cdot \frac{\sin(\pi/3 - \theta)}{\sin(\pi/3)} = m \frac{\sin(\pi/3 - \theta)}{\sin(\pi/3)}$$

$$d_{\beta} = \frac{V_{ref}}{V_{DC}} \cdot \frac{\sin \theta}{\sin(\pi/3)} = m \frac{\sin \theta}{\sin(\pi/3)}$$

$$d_{0} = 1 - d_{\alpha} - d_{\beta}$$
(7)

where,

$$d_{\alpha} = \frac{T_1}{T_s}, d_{\beta} = \frac{T_2}{T_s}, d_0 = \frac{T_0}{T_s}$$

m - modulation index

This gives switching times T_0 , T_1 and T_2 for each inverter state for a total switching period, T_s . Applying both active and zero vectors for the time periods given in (7) ensures that average voltage has the same magnitude as desired.

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4. Computation of modulating function

The four modulating functions, m_0 , m_1 , m_2 and m_3 , in terms of the duty cycle for the space vector modulation scheme can be expressed as,

$$m_0 = \frac{a_0}{2}$$

$$m_1 = m_0 + d_{\alpha}$$

$$m_2 = m_1 + d_{\beta}$$

$$m_3 = m_0 + d_{\beta}$$
(8)

5. Generation of SVPWM pulses

The required pulses can be generated by comparing the modulating functions with the triangular waveform. A symmetric seven segment technique is used to alternate the null vector in each cycle and to reverse the sequence after each null vector. The switching pulse pattern for the three phases in the six sectors can be generated. A typical seven segment switching sequence for generating reference vector in sector one is shown in Fig. 8.



Eqns. 7 and 8 show that characteristics of the SVPWM output waveform is defined by two factors, the switching frequency and the modulation index. Switching frequency desires the number of switching angles per quarter cycle, N and modulation index controls the values of these switching angles, α_1 , α_2 , α_3 ,..... The mathematical model of the SVPWM is developed and simulated using MATLAB/ Simulink to investigate the performance of a three phase inverter by the same authors in [27]. The solution trajectories for SVPWM switching angles by using the above model for N = 5 and N = 7 is shown in section IV.

III. OFFLINE PREPARATION FOR OPTIMAL PWM STRATEGY

The undesirable lower order harmonics can be eliminated by the popular selective harmonic elimination method, which is based on the harmonic elimination theory developed by Patel [14]. Fig. 9 shows the gating signals and output voltage of an inverter, in which S₁ is turned on at various switching angles $\alpha_1, \alpha_3, \ldots, \alpha_{N-I}$ and S₄ is turned off at $\alpha_2, \alpha_4, \ldots, \alpha_N$ per quarter cycle. At each instant, three required firing pulses are generated for upper switches. These three switches can be simply inverted to obtain the other three pulses for bottom switches. The firing commands are programmed in such a way that they provide three-phase symmetry, half-wave symmetry and quarter-wave symmetry. As shown in Fig. 10, the quarter-wave symmetry is preserved which will eliminate the even harmonics. The fundamental component can be controlled and a selected low order harmonics can be eliminated by the proper choice of switching angles.

By applying Fourier series analysis, the output voltage can be expressed as,

$$V(t) = \sum_{n=1,3,5}^{\infty} \frac{4}{n\pi} [V_1 \cos(n\alpha_1) \pm V_2 \cos(n\alpha_2) \pm ... \pm V_N \cos(n\alpha_N)] \sin(n\omega t)$$
(9)

where, N is the number of switching angles per quarter cycle, and $V_1, V_2 \dots V_N$ are the level of DC voltages. In this expression, the positive sign implies the rising edge, and the negative sign implies the falling edge. The solution must satisfy the condition.

$$0 \leq \alpha_1 \leq \alpha_2 \leq \dots \leq \alpha_N \leq \frac{\pi}{2}$$

However, if the switching angles do not satisfy the condition, this method no longer exists. From (9), it can be seen that the output voltage has no even harmonics because the output voltage waveform is odd quarter-wave symmetry. It can also be seen that the peak values of these odd harmonics are expressed in terms of the switching angles α_1 , α_2 ,..., α_N . Based on the harmonic elimination theory, for eliminating the n^{th} harmonics,

$$\cos(n\alpha_1) \pm \cos(n\alpha_2) \pm \dots \pm \cos(n\alpha_N) = 0 \tag{10}$$

Therefore, an equation with N switching angles will be used to control the N different harmonic values. Generally, an equation with N switching angles is used to determine the fundamental frequency value, and to eliminate N-1 low order harmonics [26]. The set of equations given by (9) is



nonlinear since they are trigonometric functions of the variables $\alpha_1, \alpha_2, \ldots, \alpha_N$. Newton's iteration method is the convenient way to calculate the angles [11]. In this, *N* equations can be written in vector form as,

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$$f_{i}(\alpha_{1},\alpha_{2}...\alpha_{N}) = \begin{bmatrix} f_{1}(\alpha_{1},\alpha_{2}...\alpha_{N}) \\ f_{2}(\alpha_{1},\alpha_{2}...\alpha_{N}) \\ \dots \\ f_{M}(\alpha_{1},\alpha_{2}...\alpha_{N}) \end{bmatrix}$$
(11)

it can be linearized by,

$$f_i(\alpha_1, \alpha_2, \dots, \alpha_N) = f(\alpha_{1,0}, \alpha_{2,0}, \dots, \alpha_{N,0}) + gradf(\alpha_1, \alpha_2, \dots, \alpha_N)\delta\alpha \quad (12)$$

The algorithm for the Newton's method is as follows:

1) Assign a set of initial values for switching angles.

$$\alpha_0 = \lfloor \alpha_{1,0}, \alpha_{2,0}, \dots, \alpha_{N,0} \rfloor$$

- 2) Calculate, $f(\alpha_0)$
- 3) Determine grad function
- 4) Compute $\delta \alpha$ by using Gauss elimination method $f(\alpha_{1,0}, \alpha_{2,0}, \dots, \alpha_{N,0}) + gradf(\alpha_1, \alpha_2, \dots, \alpha_N)\delta\alpha = 0$
- 5) Update the value of α

$$\alpha_0 = \alpha_0 + \delta \alpha$$

6) Repeat (1) to (5) until $\delta \alpha$ converges to a very small value.

These can be achieved in offline and the values of switching angles for particular modulation index are stored as look up tables. These tables are stored in a programmable memory and for example, microcomputer or microcontroller board which has been programmed to accept the value of modulation index and generate the corresponding switching angles. A model for optimal PWM scheme for three phase inverter is developed and simulated using MATLAB by the same authors in [28]. The output of the optimal PWM strategy for N = 5 and N = 7 is presented as a set of switching angles solution trajectories, which define the optimal switching angles of the inverter switches for any given modulation index between 0 and 1.0 in Figs. 11 and 13. Similarly, it is also possible to compute with N = 2, 3, 4, 6, ..., .

IV. ONLINE STRATEGY USING CFT

The switching angle solution trajectories for SVPWM (α') and optimal PWM strategy (α) by offline method are functions of modulation index for a particular value of *N* as shown in Figs. 11 and 13. Modification value ($\Delta \alpha$) can be expressed as the difference of the above two.

$$\Delta \alpha = \alpha - \alpha' \tag{13}$$

Figs. 12 and 14 show the modification values, $\Delta \alpha_1$, $\Delta \alpha_2$, $\Delta \alpha_3$,..... as a function of modulation index for N = 5 and N = 7 respectively, which are slightly nonlinear. CFT is adopted to obtain polynomial functions for the above modification values of switching angles, in terms of modulation index.

Curve fitting, also known as regression analysis, is used to find the "best fit" line or curve for a series of data points. Most of the time, the curve fit will produce an equation that can be used to find points anywhere along the curve. Least Squares is a method of curve fitting that has been popular for a long time. Least Squares minimize the square of the error between the original data and the values predicted by the equation.

In this work, polynomial curve fit is adopted to represent the switching angle trajectories. In MATLAB, p=polyfit(x,y,n) finds the coefficients of a polynomial p(x)of degree 'n' that fits the data, p(x(i)) to y(i), in a least squares sense. The result 'p' is a row vector of length n+1containing the polynomial coefficients in descending powers:

$$p(x) = p_1 x^n + p_2 x^{n-1} + \dots + p_n x + p_{n+1}$$
(14)

where,

 p_1, p_2, p_3, \dots are polynomial coefficients.

For more complex curvature, higher the polynomial order is required to fit. There are no data restrictions associated with polynomial curve fit. Quadratic polynomial functions are implemented in the present work. The polynomial functions for N = 5 and N = 7 from Figs. 12 and 14 using MATLAB are given in Eqns. 15 and 16.





Fig. 12. Modification values for switching angles Vs modulation index for N = 5



Fig. 14. Modification values for switching angles Vs modulation index for N = 7 $\,$

 $\underbrace{Quadratic \ equations \ for \Delta\alpha}_{A_{1}} (N = 7) \\
 \Delta\alpha_{1} = 12.56m^{2} - 4.056m + 3.725 \\
 \Delta\alpha_{2} = 11.95m^{2} + 6.854m + 3.177 \\
 \Delta\alpha_{3} = -6.092m^{2} - 3.402m + 5.844 \\
 \Delta\alpha_{4} = -7.452m^{2} + 5.091m + 4.349 \\
 \Delta\alpha_{5} = 3.107m^{2} + 5.905m - 4.246 \\
 \Delta\alpha_{6} = 7.342m^{2} - 5.478m - 6.565 \\
 \Delta\alpha_{7} = 3.106m^{2} - 1.066m + 0.229$ (16)

V. HARMONIC ANALYSIS TECHNIQUE

To measure the harmonic performance, Weighted Total Harmonic Distortion (WTHD) method is used. The WTHD defined in Eq. (17) gives the rms value of the harmonic voltages normalized to the maximum fundamental voltage and divided by the harmonic number. It is calculated in the same way as THD, but each harmonic component is divided by its order, so that higher order harmonics receive lower weight and contribute less in this figure of merit [28]. An upper limit of 50 is often recommended for the calculation of the WTHD as suggested in the IEEE standard 519 [29]. The definition of WTHD in an inverter circuit is given as,

$$WTHD = \frac{\sqrt{\sum_{n=2}^{\infty} \left(\frac{V_n}{n}\right)^2}}{V_1}$$
(17)

where, V_n is the harmonic component for the n^{th} harmonic and V_l is the fundamental component.

VI. SIMULATION MODEL

A three-phase inverter with a balanced star connected RL load is considered. A complete mathematical model of the SVPWM is developed and simulated using MATLAB/ Simulink to investigate the performance of a three phase inverter in [27]. The same model is modified by implementing the proposed online method to eliminate the low order harmonics. The proposed online method is modeled as a subsystem in MATLAB as shown in Fig. 15. The overall model of the SVPWM three phase inverter with online elimination of harmonics is shown in Fig. 16. For motor drive applications, it needs to eliminate low order harmonics only up to 19. Hence, the 5th, 7th, 11th, 13th, 17th and 19th harmonics need to be theoretically eliminated from line voltage. So, the model is developed for N = 2 to 7 and the Eq. (10) can be rewritten as,

$$m = \cos(\alpha_{1}) + \cos(\alpha_{2}) + \dots + \cos(\alpha_{N-1})$$

$$0 = \cos(5\alpha_{1}) + \cos(5\alpha_{2}) + \dots + \cos(5\alpha_{N-1})$$

$$0 = \cos(7\alpha_{1}) + \cos(7\alpha_{2}) + \dots + \cos(7\alpha_{N-1})$$

:
:

$$0 = \cos(17\alpha_{1}) + \cos(17\alpha_{2}) + \dots + \cos(17\alpha_{N-1})$$

$$0 = \cos(19\alpha_{1}) + \cos(19\alpha_{2}) + \dots + \cos(19\alpha_{N-1})$$

(18)

where,

m is the modulation index defined as

$$m = \frac{\pi V_1}{4V_{DC}} \qquad (0 < m \le 1) \tag{18}$$



Fig.16. MATLAB/Simulink model of SVPWM three phase inverter

Solution of the above set of nonlinear equation determines the *N-1* switching angles, namely, α_1 , α_2 , α_3 , α_4 , α_5 α_{N-1} .

Sector corresponds to the location of voltage in the circular locus traced by it and is divided into six sectors of 60° each. In a two level voltage source inverter, the voltage levels are $+V_{DC}/2$ and $-V_{DC}/2$, where V_{DC} is the DC link voltage. The line voltage alternates between $+V_{DC}$ and $-V_{DC}$ while the phase voltage can assume the values of $\pm 2V_{DC}/3$ and $\pm V_{DC}/3$. The model was run according to Table I.

SIMULATION PARAMETERS								
$\frac{\text{DC}}{\text{Voltage}(V_{DC})}$	Fundamental	Switching		R	L			
	Frequency(f)	Frequency(fs)	т	(Ω)	(mH)			
400 V	50 Hz	200 Hz – 800 Hz	0.1 - 0.8	10.0	15.0			

TADLE

VII. SIMULATION RESULTS AND DISCUSSION

Figs. 17, 18 and 19 show the generated phase voltage, line voltage and pole voltage respectively after the harmonic elimination for N = 7. Fig. 20 shows the line current, because of the inductive nature of the load, higher order harmonics have been filtered out and the current waveform is sinusoidal in nature.



Fig. 21.a shows harmonic spectrum of line voltage of SVPWM before harmonic elimination for m = 0.8 and N = 5. Fig. 21.b shows the harmonic spectrum for optimal PWM for m = 0.8 and N = 5 using offline method after the selected harmonic elimination of 5th, 7th, 11th and 13th harmonics. It can be perceived that the first remaining harmonic, i.e. the 17th has a large magnitude, which is a peculiar characteristic of HEPWM, where the energy that would have been associated with those eliminated harmonics is transferred to

the first remaining harmonic [31]. Fig. 21.c shows harmonic spectrum of line voltage using online method by the proposed model for the same parametric values. It can be observed that all the selected harmonics are effectively eliminated. This ensures the accuracy of the model in eliminating low order harmonics when comparing with the offline method. Table II gives the normalized magnitudes of line voltage for m = 0.2, m = 0.4 and m = 0.8 with N = 5, which shows that harmonic performance is more or less uniform in the modulation index ranging from 0.1 to 0.9. Fig. 22.a shows harmonic spectrum of line voltage of SVPWM before harmonic elimination for m = 0.8 and N =7. Fig. 22.b shows the harmonic spectrum for SHEPWM for m = 0.8 and N = 7 using offline method after the selected harmonic elimination of 5th, 7th, 11th, 13th, 17th and 19th harmonics, while Fig. 21.c shows harmonic spectrum of line voltage using online method by the proposed model for the same parametric values. Table III shows the normalized magnitudes of line voltage for m = 0.2, m = 0.4 and m = 0.8with N = 7, which ensures the uniformity of harmonic performance in the modulation index ranging from 0.1 to 0.9.



 TABLE II.

 NORMALISED MAGNITUDE OF LINE VOLTAGE FOR N = 5

11	Modulation index									
Harm	m = 0.2			m = 0.4			m = 0.8			
onic	SV HEPWM		SV HEPWM		SV HEPWM					
order	PWM	offline	online	PWM	offline	online	PWM	offline	online	
1	1.00	1.00	1.00	1.00	1.00	1.00	1.00	1.00	1.00	
5	0.00	0.00	0.01	0.03	0.00	0.00	0.16	0.00	0.00	
7	0.04	0.00	0.02	0.08	0.00	0.01	0.16	0.00	0.01	
11	0.87	0.00	0.03	0.68	0.00	0.01	0.18	0.00	0.01	
13	0.16	0.00	0.00	0.27	0.00	0.00	0.10	0.00	0.02	
17	0.13	0.82	0.82	0.18	0.38	0.39	0.05	0.30	0.32	
19	0.93	0.85	0.86	0.52	0.51	0.51	0.14	0.06	0.05	
23	0.23	0.10	0.12	0.25	0.28	0.27	0.21	0.14	0.10	
25	0.12	0.07	0.07	0.29	0.17	0.17	0.10	0.06	0.06	
29	0.75	0.06	0.08	0.04	0.11	0.12	0.14	0.07	0.05	
31	0.41	0.09	0.08	0.12	0.15	0.15	0.02	0.05	0.07	
35	0.20	0.42	0.43	0.25	0.22	0.22	0.04	0.10	0.12	
37	0.18	0.47	0.49	0.10	0.11	0.11	0.08	0.08	0.07	
41	0.17	0.22	0.23	0.15	0.10	0.11	0.11	0.02	0.08	
43	0.24	0.15	0.13	0.08	0.11	0.12	0.06	0.05	0.06	
47	0.14	0.13	0.11	0.22	0.06	0.07	0.07	0.14	0.11	
49	0.25	0.17	0.17	0.17	0.2	0.01	0.02	0.06	0.05	



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NORMALISED MAGNITUDE OF LINE VOLTAGE FOR $N = 7$									
11	Modulation index								
Harm onic order	m = 0.2			m = 0.4			m = 0.8		
	SV	HEPWM		SV HEP		WM	SV	HEPWM	
order	PWM	offline	online	PWM	offline	online	PWM	offline	online
1	1.00	1.00	1.00	1.00	1.00	1.00	1.00	1.00	1.00
5	0.20	0.00	0.04	0.16	0.00	0.01	0.20	0.00	0.02
7	0.18	0.00	0.03	0.04	0.00	0.01	0.02	0.00	0.03
11	0.32	0.00	0.05	0.23	0.00	0.00	0.15	0.00	0.02
13	0.81	0.00	0.05	0.68	0.00	0.00	0.23	0.00	0.04
17	0.19	0.00	0.06	0.31	0.00	0.01	0.16	0.00	0.01
19	0.19	0.00	0.06	0.21	0.00	0.01	0.19	0.00	0.01
23	0.06	0.84	0.83	0.13	0.40	0.41	0.11	0.27	0.22
25	0.45	0.90	0.89	0.22	0.61	0.59	0.03	0.01	0.05
29	0.89	0.03	0.04	0.40	0.17	0.15	0.12	0.03	0.03
31	0.40	0.11	0.11	0.32	0.25	0.27	0.17	0.14	0.16
35	.03	0.05	0.03	0.10	0.06	0.06	0.01	0.07	0.10
37	0.7	0.06	0.04	0.16	0.13	0.14	0.05	0.04	0.03
41	0.27	0.08	0.06	0.07	0.15	0.18	0.05	0.04	0.08
43	0.63	0.01	0.06	0.01	0.00	0.03	0.14	0.07	0.07
47	0.27	0.50	0.35	0.17	0.14	0.08	0.01	0.14	0.13
49	0.06	0.59	0.54	0.11	0.05	0.05	0.02	0.06	0.02

TABLE III.

The variation of WTHD in line voltage as a function of modulation index for SVPWM and HEPWM by offline method are shown in Figs. 23 and 24 for N = 5 and N = 7 respectively. These curves show that a marginal improvement of WTHD is possible for the modulation index ranging from 0.1 to 0.9. Figs. 23 and 24 also reveal that the graphs for HEPWM by offline and online methods are coinciding in almost all the ranges of modulation indices. Table IV shows WTHD for m = 0.2, m = 0.4 and m = 0.8 with N = 5 and N = 7. These results ensure the effectiveness of the proposed model in generating a high quality PWM waveform. Similar results are generated for N = 2, 3, ..., which are not included in this paper. It is examined that the model can perform precisely for a wide range of switching frequency from 200 Hz onwards.

TABLE IV
OMDADISON OF WITHD

COMPARISON OF WITHD									
	m = 0.2			m = 0.4			m = 0.8		
N SV		HEP	WM	SV	HEPWM		SV	HEPWM	
Р	PWM	offline	online	PWM	offline	online	PWM	offline	online
5	10.0%	6.89%	6.95%	7.51%	3.89%	3.94%	4.53%	1.99%	2.03%
7	9.50%	5.34%	5.36%	7.10%	3.19%	3.20%	4.90%	1.33%	1.36%







Fig. 24. Line voltage WTHD Vs Modulation index for N = 7

V. CONCLUSIONS

This paper proposed a new procedure for an efficient and fast online method for the harmonic elimination of SVPWM for three phase inverter. The SVPWM output waveform is modified for an optimal PWM waveform by applying modification values to the switching angle pattern of SVPWM, which are calculated online using polynomial equations. These polynomial equations are functions of modulation index and the coefficients are determined using curve fitting technique. Output waveforms of MATLAB/Simulink model for SVPWM three phase inverter is generated. Harmonic performance of the modified output waveform is carried out by comparing the WTHD of the same with that of offline method.

The simulation results show that the proposed online method is precise as offline method in eliminating the low order harmonics and reducing the total harmonic distortion (using WTHD). Comparing with other conventional online methods, in the proposed method there is no need to save all the optimal switching angles in the form of look-up tables. Instead, only the coefficients of quadratic functions are saved in the memory, which requires less memory space. The online computation of the quadratic polynomial functions are simple which need only the addition and multiplication processes, allowing its practical implementation using low cost microprocessors and DSPs. The developed model allows the user to control the voltage in a wide range or the number of switching angles by varying the switching frequency according to their design requirements. Considering the accuracy and effectiveness, the proposed inverter model has considerable practical use for variable-speed AC motor drive applications, where harmonics in the output of the inverter pose serious problems in the motor performance. The model can be adapted for applications like UPS with fixed frequency or in application where the inverter has to track the grid.

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REFERENCES

- B. K. Bose, "Adjustable speed AC drives A technology status review," *Proceedings of the IEEE- IECON*, Vol. 70, pp. 116-135, February 1982.
- [2] D. G. Holmes and T. A. Lipo, "Pulse Width Modulation for Power Converters: Principles and Practice," *New Jersey: Wiley* IEEE Press, 2003.
- [3] J. Holtz, P. Lammert and W. Lotzkat, "High speed drive system with ultrasonic MOSFET PWM inverter and single-chip microprocessor control," *IEEE Transactions on Industry Applications*, Vol. 23, No.6 pp. 1010-1015, November 1987.
- [4] J. Holtz, "Pulse width modulation a survey," *IEEE Transactions on Industrial Electronics*, Vol. 39, No. 5, pp. 410-420, October 1992.
- [5] H. W. Van der Broeck, H. C. Skudelny and G.V. Stanke," Analysis and realisation of a pulse width modulator based on voltage space vectors", *IEEE Transactions on Industry Applications*, Vol. 24, pp. 142-150, January/February 1988.
- [6] A. Mehrizi-Sani and S. Filizadeh, "Digital Implementation and Transient Simulation of Space Vector Modulated Converters," *IEEE Power Engineering Society General Meeting*, Montreal, QC, Canada, June 2006.
- [7] Fang Zheng Peng, Jih-Sheng Lai, et. al, "A Multilevel Voltage-Source Inverter with Separate DC Sources for Static Var Generation," *IEEE Transactions on Industry Applications*, Vol. 32, No. 5, pp. 1130-1138, September/October 1996.
- [8] Jose Rodriguez, J. S. Lai, and F. Z. Peng, "Multilevel Inverters: A Survey of Topologies, Controls, and Applications," *IEEE Transactions on Industrial Electronics*, Vol. 49, No. 4, pp. 724-738, August 2002.
- [9] O. Lopez, J. Alvarez, J. Doval-Gandoy, and F. D. Freijedo, "Multilevel multiphase space vector PWM algorithm," *IEEE Transactions on Industrial Electronics*, Vol. 55, No. 5, pp. 244-251, May 2008.
- [10] J. F. Moynihan, M. G. Egan and J. M. D. Murphy," Theoretical spectra of space vector modulated waveforms," *IEE Proc. Electrical Power Applications*, Vol. 145, No. 1, January 1998.
- [11] J. W. Chen and T. J. Liang, "A Novel Algorithm in Solving Nonlinear Equations for Programmed PWM Inverter to Eliminate Harmonics," 23rd International Conference on Industrial Electronics, control and Instrumentation IEEE IECON '97, Vol. 2, pp. 698-703, November 1997.
- [12] J. N. Chiasson, L. M. Tolbert, K. J. McKenzie and Z. Du, "Elimination of harmonics in a multilevel converter using the theory of symmetric polynomials resultants," *IEEE Transactions on Control System Technology*, Vol. 13, No. 2, pp. 216-223, March 2005.
- [13] B. Ozpineci, L. M. Tolbert and J. N. Chiasson, "Harmonic optimization of multilevel converters using genetic algorithms, *IEEE Power Electronics Letters*, Vol. 3, No. 3, pp. 92-95, September 2005.
- [14] H. S. Patel and R. G. Hoft, "Genealized techniques of harmonic elimination and voltage control in thyristor inverters: Part I-Harmonic elimination," *IEEE Transactions on Industry Applications*, Vol. 9, No. 3, pp. 310-317, May/June 1973.
- [15] Q. Jiang, D. Grahame Holmes, David B. Giesner, "A Method for Linearising Optimal PWM Switching Strategies to Enable their Computation On-line in Real-time," *Conference Proceedings of the IEEE Industry Applications Society Annual Meeting* '91, Vol. 1, pp. 819-825, September/October 1991.
- [16] Taufiq, J. A., Mellitt, B and Goodman, C. J., "Novel Algorithm for Generating Near Optimal PWM waveforms for AC Traction Drives", *IEE Proceedings B on Electric Power Applications*, Vol. 133, No. 2, pp. 85-94, March 1986.
- [17] S. R. Bowes, "Advanced Regular Sampled PWM control techniques for drives and static power converters," *IEEE Transactions on Industrial Electronics*, Vol. 42, No. 4, pp. 367-373, August 1995.
- [18] S. R. Bowes, "Novel space vector based harmonic elimination inverter control," *IEEE Transactions on Industry applications*, Vol. 36, No. 2, pp. 549-557, March/April 2000.

- [19] J. N. Chiasson, L. M. Tolbert, K. J. McKenzie and Z. Du, "A complete solution to the Harmonic Elimination Problem," *IEEE Transactions on Power Electronics*, Vol. 19, No. 2, pp. 3-9, March 2004.
- [20] Nguyen Van Nho and Myung Joong Youn, "A Simple Online SHEPWM with Extension to Six Step Mode in Two Level Inverters," *IEEE International Conference on Power Electronics and drive Systems-PEDS'05*, Vol. 2, pp. 1419-1424, November 2005.
- [21] A. I., Maswood, "PWM SHE Switching Algorithm for Voltage Source Inverter," *IEEE International Conference on Power Electronics, Drives and Energy Systems-PEDES'06*, pp. 1-4, December 2006.
- [22] Yu Liu, Hoon Hong and Alex Q. Huang, "Real-time Calculation of Switching Angles Minimizing THD for Multilevel Inverters with Step Modulation," *IEEE Transactions on Industrial Electronics*, Vol. 56, No. 2, pp. 285-293, February 2009.
- [23] Zhi Zeying Liu Hui and Han Rucheng. "CFT based on-line calculation for Optimal PWM switching angles", *IEEE Conference* on Power and Energy Engineering – APPEEC'09, pp. 1-5, March 2009.
- [24] N. Ahmed Azli and A. H. M. Yatim, "A Curve Fitting Technique (CFT) for Optimal PWM Online Control of a Modular Structured Multilevel Inverter (MSMI)", 4th IEEE International conference on Power Electronics and Drive Systems, Vol. 2, pp. 598-604, October 2001.
- [25] N. Azli, A. H. M. Yatim, "Curve fitting technique for optimal pulsewidth modulation(PWM)online control of a voltage source inverter (VSI)", *Proceedings of TENCON 2000*, Vol. 1, pp. 419-422. September 2000.
- [26] V. G. Agelidis, A. I. Balouktsis, and M. S. A. Dahidah, "A five level symmetrically defined selective harmonic elimination PWM strategy: Analysis and experimental validation," *IEEE Transactions* on *Power Electronics*, vol. 23, no. 1, pp. 19-26, January 2008.
- [27] G. K. Nisha, S. Ushakumari and Z. V. Lakaparampil, "Harmonic Elimination of Space Vector Modulated Three Phase Inverter", *Lecture Notes in Engineering and Computer Science: Proceedings of the International Multi-conference of Engineers and Computer Scientists 2012, IMECS 2012*, 14-16 March, 2012, Hong Kong, pp. 1109-1115.
- [28] G. K. Nisha, S. Ushakumari and Z. V. Lakaparampil, "Method to Eliminate Harmonics in PWM: A Study for Single Phase and Three Phase", *International conference on Emerging Technology, Trends* on Advanced Engineering Research, Vol. 2, pp. 598-604, February 2012.
- [29] A. Mehrizi-Sani and S. Filizadeh, "An optimized Space Vector Modulation Sequence for Improved Harmonic Performance," *IEEE Transactions on Industrial Electronics*, Vol. 56, No. 8, pp. 2894-2903, August 2009.
- [30] IEEE Recommended Practices and Requirements for Harmonic Control in Electrical Power Systems, IEEE Standard 519, 1992.
- [31] S. R. Bowes and D. Holliday, "Optimal Regular Sampled PWM inverter control techniques," *IEEE Transactions on Industrial Electronics*, Vol. 54, pp. 1547-1559, June 2007.



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