Fast Algorithm in ECC for Wireless Sensor Network

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Abstract—Elliptic curve cryptography (ECC) has been attractive to the people who are working in the field of the network security due to its good potential for wireless sensor network security due to its smaller key size and its high strength of security. But there is a room to reduce the key calculation time to meet the potential applications, in particular for wireless sensor networks (WSN). It is well known that scalar multiplication is the operation in elliptical curve cryptography which takes 80% of key calculation time on wireless sensor network motes. In this paper, the research proposes algorithm based on 1's complement subtraction to represent scalar in scalar multiplication which offer less Hamming weight and will remarkably improve the computational efficiency of scalar multiplication.

Index Terms—Elliptic curve cryptography, Scalar multiplication, Non-adjacent form, Hamming weight, one's complement subtraction, ROM, wireless sensor networks

I. INTRODUCTION

Rapid growth in the very large scale integrated (VLSI) technology, embedded systems and micro electro mechanical systems (MEMS) has enabled production of less expensive sensor nodes which can communicate information shorter distances with efficient use of power [1]. Sensor node detects information, processes it with the help of an in-built microcontroller and communicates results to the 'sink or base station'. The base station is a more powerful node linked with central station via satellite or internet communication. Wireless sensor networks can be deployed in various applications namely environmental monitoring e.g. volcano detection [2,3], distributed control system [4], detection of radioactive sources [5], agricultural and farm management [6, 17], and computing platform for tomorrows' internet[7].

II CHALLENGES IN DEVELOPING SECURED PROTOCOLS FOR WIRELESS SENSOR NETWORKS

Compared to traditional networks, a wireless sensor network has many resource constraints [4]. The MICA2 mote consists of an 8 bit ATMega 128L microcontroller working on 7.3 MHz. As a result nodes of WSN have limited computational power. Normally, radio transceiver of MICA motes can achieve maximum data rate of 250 Kbits/sec which puts a limitation on the communication resources. The flash memory which is available on the MICA mote is only 512 Kbyte. Apart from these the battery which is available on the board is of 3.3.V with 2A-Hr capacity. Due to the above boundaries the current state of art protocols and algorithms are expensive for sensor networks due to their high communication overheads.

III ELLIPTIC CURVE CRYPTOGRAPHY

PRELIMINARIES

Elliptic Curve Cryptography was introduced by Victor Miller [9] and Neal Koblitz [10] independently in the early eighties. The advantage of ECC over other public key cryptography techniques such as RSA, Diffie-Hellman is that the best known algorithm for solving ECDLP the underlying hard mathematical problem in ECC takes the fully exponential time. On the other hand the best algorithm for solving RSA and Diffie-Hellman takes sub exponential time [11]. To sum up the problem of ECC can be solved only in exponential time and so far there is a lack of sub exponential attack on ECC.

An elliptic curve *E* over GF(p) can be defined by $y^2 = x^3 + ax + b$ where $a, b \in GF(p)$ and

$$4a^3 + 27b^2 \neq 0 \quad \text{in the } GF(p) \tag{1}$$

The point (x, y) on the curve satisfies above equation and the point at infinity denoted by ∞ is said to be on the curve. Let's have examples:



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It is noted that they are in different "shapes', Figure 1 is only part as the whole elliptic curve but Figure 2 has two part as the whole elliptic curve.

If there are two points on curve namely, $P(x_1, y_1)$, $Q(x_2, y_2)$ and their sum given by point $R(x_3, y_3)$ the algebraic formulas for point addition and point doubling are given by following equations:

$$x_{3} = \lambda^{2} - x_{1} - x_{2}$$

$$y_{3} = \lambda(x_{1} - x_{3}) - y_{1}$$

$$\lambda = \frac{y_{2} - y_{1}}{x_{2} - x_{1}}, \text{ if } P \neq Q$$

$$\lambda = \frac{3x^{2} + a}{2y_{1}}, \text{ if } P = Q$$

where the addition, subtraction, multiplication and the inverse are the arithmetic operations over GF(p), which can be shown in Fig. 1 and Fig 2.

IV ELLIPTIC CURVE DIFFIE-HELLMAN SCHEME (ECDH) PROPOSED FOR WSN

As per [13] the original Diffie-Hellman algorithm with RSA requires a key of 1024 bits to achieve sufficient security but *Diffie-Hellman based on ECC* can achieve the same security level with only 160 bit key size.

The classical Elliptic Curve Diffie-Hellman scheme works as shown in the Fig. 3



Fig. 3 Diffie-Hellman protocol based on ECC

Initially Alice and Bob agree on a particular curve with base point *P*. They generate their public keys by multiplying *P* with their private keys namely K_A and K_B . After sharing public keys, they generate a shared secret key by multiplying public keys by their private keys. The secret key is $R = K_A * Q_B = K_B * Q_A$. With the known values of Q_A , Q_B and *P* it is computationally intractable for an eavesdropper to calculate K_A and K_B which are the private keys of Alice and Bob. As a result, adversaries cannot figure out *R* which is the shared secret key.

V CHALLENGES INVOLVED IN THE SCALAR MULTIPLICATION

In ECC two heavily used operations are involved namely, scalar multiplication and modular reduction. The Gura et al [14] showed that 85% of execution time is spent on scalar multiplication operation. Scalar Multiplication is the operation

Of multiplying point P on an elliptic curve E defined over a field GF(p) with positive integer k which involves point addition and point doubling. Operational efficiency of kP is affected by the type of coordinate system used for point P on the elliptic curve and the algorithm used for recoding of integer k in scalar multiplication.

This research paper proposes innovative algorithm based on one's complement for representation of integer k which accelerates the computation of scalar multiplication in wireless sensor networks.

VI RECODING OF INTEGER *k* IN SCALAR MULTIPLICATION

The number of point doubling and point additions in scalar multiplication depends on the recoding of integer k. Expressing integer k in binary format highlight this dependency.

The number of zeros and number of ones in the binary form, their places and the total number of bit affects computational cost of scalar multiplications. The Hamming weight i.e. the number of non-zero elements, determines the number of point additions and bit length of integer K determines the number of point doublings operations in scalar multiplication.

One point addition when $P \neq Q$ requires one field inversion and three field multiplications [13,16,18]. Squaring is counted as regular multiplications. This cost is denoted by 1I + 3M, where the *I* denotes the cost of inversion and *M* denotes the cost of multiplication.

One point doubling when P = Q requires 1I + 4M as we can neglect the cost of field additions as well as the cost of multiplications by small constant 2 and 3 in the above formulae. Also for the various standards can be found in "Cryptographic Toolkit" [15].

VII THE EXISTING METHODS OF SCALAR MULTIPLICATION

1. BINARY METHOD

Scalar multiplication is the computation of the form Q = kP, where *P* and *Q* are the elliptic curve points and *k* is positive integer. This is achieved by repeated elliptic curve point addition and doubling operations. In binary method the integer *k* is represented in binary form:

$$k = \sum_{j=0}^{l-1} K_j 2^j, \ K_j \in \{0,1\}$$

The Binary method scans the bits of K either from left-to-right or right-to-left. The binary method for the computation of kP is given in the following Algorithm 1, as shown below:

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Algorithm 1: Left to right binary method for point multiplication

Input: A point $P \in E(\mathcal{F}_q)$, an		
<i>l</i> bits integer $k = \sum_{j=0}^{l-1} K_j 2^j, K_j \in \{0,1\}$		
Output: $Q = kP$		
1. $Q \leftarrow \infty$		
2. For $j = l - 1$ to 0 do:		
2.1 $Q \leftarrow 2 Q$,		
2.2 if $k_i = 1$ the $Q \leftarrow Q + P$.		
3. Return Q.		

The cost of multiplication in binary method depends on the number of non zero elements and length of the binary representation of k. If the representation has $k_{l-1} \neq 0$ then binary method require (l - 1) point doublings and (W-1) where l is the length of the binary expansion of k and W is the Hamming weight of the k that is the number of non-zero elements in expansion of k.

For example if $k = 629 = (1001110101)_2$, it will require (W-1) = 6 - 1 = 5 point additions and l - 1 = 10 - 1 = 9 point doublings operations.

2. SIGNED DIGIT REPRESENTATION METHOD

The subtraction has virtually same cost as addition in the elliptic curve group. The negative of point (x, y) is (x, -y) in odd characteristics. This leads to scalar multiplication methods based on addition –subtraction chains, which help to reduce the number of curve operations. When integer k is represented with the following form, it is called *as binary signed digit representations*.

$$k = \sum_{j=0}^{l} S_j 2^j, \ S_j \in \{1,0,-1\}$$

When signed digit representation has no adjacent non zero digits, i.e. $S_j S_{j+1} = 0$ for all $j \ge 0$ it is called non-adjacent from (NAF).

The following Algorithm 2 computes the NAF of a positive integer given in binary representation.

Algorithm 2: Conversion from Binary to NAF

Input: An integer
$$k = \sum_{j=0}^{l-1} K_j 2^j$$
, $K_j \in \{0,1\}$
Output: NAF $k = \sum_{j=0}^{l} S_j 2^j$, $S_j \in \{1,0,-1\}$
1. $C_0 \leftarrow 0$
2. For $j = 0$ to l do:
3. $C_{j+l} \leftarrow [(K_j + K_{j+1} + C_j)/2]$
4. $S_j \leftarrow K_j + C_j - 2C_{j+1}$
5. Return $(S_1 \dots S_0)$

NAF has usually fewer non zero digits than binary representations. The average hamming weight for NAF form is (n-1)/3.0. So generally it requires (n-1) point doublings and (n-1)/3.0 point additions. The binary method can be revised accordingly and is given another algorithm for NAF form, and this modified method is called as *Addition Subtraction* method.

VIII PROPOSED ALGORITHM BASED ON ONE'S COMPLEMENT FOR RECODING OF SCALAR K

A subtraction by utilization of the 1's complement is most common in binary arithmetic. The 1's complement of any binary number may be found by the following equation [19]:

$$C_1 = (2^a - 1) - N \tag{I}$$

where $C_1 = 1$'s complement of the binary number a = number of bits in N in terms of binary form N = binary number

A close observation of the equation (I) reveals the fact that any positive integer can be represented by using minimal non zero bits in its 1's complement form provided that it is having minimum of 50% Hamming weight. The minimal non zero bits in positive integer scalar are very important to reduce number of intermediate operations of multiplication, squaring and inverse in elliptical curve cryptography as we have seen in the previous sections.

The equation (I) can be modified as per below-

$$N = (2^a - C_1 - 1)$$
(II)

For example, let us take N =1788

where, $N = (11011111100)_2$ in its binary form

 C_1 = 1's Complement of the number of N= (00100000011)₂ *a* it is in binary form so we have a = 11

After putting all the above values in the equation II we will get,

 $1788 = 2^{11} - 0010000011 - 1$, this can be reduced to,

$$1788 = 10000000000000000011 - 1$$
 (III)

As evident from equation III the Hamming weight of scalar N has reduced from 8 to 5 which will save 3 elliptic curve addition operations. One addition operation requires 2 Squaring, 2 Multiplication and 1 inverse operation. In this case total 6 Squaring, 6 Multiplication and 3 Inverse operations will be saved.

The above recoding method based on one's complement subtraction combined with sliding window method gives very good optimization results. Proceedings of the International MultiConference of Engineers and Computer Scientists 2010 Vol II, IMECS 2010, March 17 - 19, 2010, Hong Kong

Algorithm for sliding window scalar multiplication on elliptic curves.

1. $Q \leftarrow P_{\infty}$ and $i \leftarrow l-1$ 2. while $i \ge 0$ do 3. if $n_i = 0$ then $Q \leftarrow [2]Q$ and $i \leftarrow i-1$ 4. else 5. $s \leftarrow \max(i - k + 1, 0)$ 6. while $n_s = 0$ do $s \leftarrow s + 1$ 7. for h = 1 to i - s + 1 do $Q \leftarrow [2]Q$ 8. $u \leftarrow (n_1, \dots, n_s)_2$ $[n_i = n_s = 1$ and $i - s + 1 \le k]$ 9. $Q \leftarrow Q \oplus [u]P$ [u is odd so that [u]P is precompute d] 10. $i \leftarrow s - 1$ 11. return Q

Let us compute [763] P (in other words k = 763) with sliding window algorithm with K recoded in Binary form with different window sizes ranging from 2 to 10. It is observed that as the window size increases the number of pre computations also increases geometrically. At the same time number of additions and doubling operations decreases.

Now we present the details for the different window size to find out the optimal window size via this example:

Window Size w = 2763 = (1011111011)₂ No of precomputations = $2^w - 1 = 2^2 - 1 = [3] P$ 763 = <u>10</u> <u>11</u> <u>11</u> <u>10</u> <u>11</u> The intermediate values of *Q* are P, 2P 4P, 8P, 11P, 22P, 44P, 47P, 94P, 95P, 190P, 380P, 760P, 763P Computational cost = 9 doublings, 4 additions, and 1 precomputation.

Window Size w = 3No of precomputations $= 2^w - 1 = 2^3 - 1 = [7] P$ The intermediate values of Q are 5P, 10P, 20P, 40P, 47P, 94P, 188P, 376P, 381P, 762P, 763P Computational cost = 7 doublings, 3 additions, and 3 precomputation.

Window Size w = 4No of precomputations $= 2^4 - 1 = [15] P$ The intermediate values of Q are 11P, 22P, 44P, 88P, 95P, 190P, 380P, 760P, 763P Computational cost = 6 doublings, 2 additions, and 7 precomputation.

Window Size w = 5No of precomputations $= 2^5 - 1 = [31] P$ The intermediate values of Q are 23P, 46P, 92P, 184P, 368P, 736P, 763P Computational cost = 5 doublings, 1 additions, and 15 precomputation.

Window Size w = 6The intermediate values of Q are 47P, 94P, 188P, 376P, 752P, 763P Computational cost = 4 doublings, 1 additions, and 31 precomputation.

Window Size w = 7The intermediate values of Q are 95P, 190P, 380P, 760P, 763P Computational cost = 3 doublings, 1 additions, and 61 precomputation.

Window Size w = 8The intermediate values of Q are 95P, 190P, 380P, 760P, 763P Computational cost = 3 doublings, 1 additions, and 127 precomputation.

Window Size w = 9763 = <u>10111101</u> <u>1</u> The intermediate values of *Q* are 381P, 762P, 763P Computational cost = 1 doublings, 1 additions, and 251 precomputation.

Window Size w = 10 763 = 101111011The intermediate values of Q are 763PComputational cost = 0 doublings, 0 additions, and 510 precomputation.

The trade-off between the computational cost and the window size are shown in Table 1.

Table 1: Window Size Vs No of doublings, additions and Pre computations

Window Size	No of Doublings	No of Additions	No of Pre computations
2	9	4	1
3	7	3	3
4	6	2	7
5	5	1	15
6	4	1	31
7	3	1	61
8	3	1	127
9	1	1	251
10	0	0	501

In Figure. 4 shows the trade off between window size and the computational costs



Fig. 4 Trade off between window size and computational

Figure 5 shows the trade off between window size and number of per-computations.

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Fig.5: Trade off between window size and number of pre computations

Now let us apply the proposed algorithm to the same number 763 to show the effectiveness of algorithm with window size of 3.

As we know that

 $763 = (10111111011)_2$

Let's have record 763 with equation II in one's complement subtraction form and we have: 763 = 1000000000 - 010000100 - 1

=10100000101

With window *w* size of 3, we obtained:

 $763 = 101 \ 000000 \ 101$

Here "<u>1</u>" means "-1"

We have:

- The intermediate values of Q are:
- 3P, 6P, 12P, 24P, 48P, 96P, 192P, 384P, 768P, 763P

Hence we the Computational Cost = 8 doublings, 1 addition and 3 pre computations.

With the equation II the computational cost has been reduced from 3 additions as in binary method to only 1 addition in one's complement subtraction form. The number of pre computations remained same. This can be proved for different window sizes.

IX CONCLUSION

The positive integer in point multiplication may be recoded with one' complement subtraction to reduce the computational cost involved in this heavy mathematical operation for wireless sensor network platforms. The window size may be a subject of trade off between the available RAM and ROM at that particular instance on sensor node. As NAF method involves modular inversion operation to get the NAF of binary number, the one's complement subtraction can provide a very simple way of recoding integer.

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