Preface of the 2017 IAENG International Conference on Electrical Engineering Special Session: Design, Analysis and Tools for Integrated Circuits and Systems

The International Workshop DATICS-IMECS'17: Design, Analysis and Tools for Integrated Circuits and Systems in the International MultiConference of Engineers and Computer Scientists 2017 will take place in Hong Kong, 15-17 March, 2017.

DATICS Workshops were initially created by a network of researchers and engineers both from academia and industry in the areas of Design, Analysis and Tools for Integrated Circuits and Systems. Recently, DATICS has been extended to the fields of Communication, Computer Science, Software Engineering and Information Technology.

The main target of DATICS-IMECS'17 is to bring together software/hardware engineering researchers, computer scientists, practitioners and people from industry to exchange theories, ideas, techniques and experiences related to all aspects of DATICS.

The International Program Committee (IPC) of DATICS-IMECS'17 consists of about 150 experts in the related fields of DATICS-IMECS'17 both from academia and industry. DATICS-IMECS'17 is partnered with CEOL: Centre for Efficiency-Oriented Languages (Ireland), KATRI (Japan & Hong Kong), Distributed Thought (UK), Theory of Computation Lab - Yonsei University (South Korea), Baltic Institute of Advanced Technology - BPTI (Lithuania), Solari (Hong Kong), Transcend Epoch (Hong Kong), Vytautas Magnus University (Lithuania) and Xi'an Jiaotong-Liverpool University - XJTLU (China - UK).

The DATICS-IMECS'17 Technical Program includes 12 lecture sessions. On behalf of the IPC, we would like to welcome the delegates and their guests to the DATICS-IMECS'17. We hope that you will enjoy the workshop and find the DATICS-IMECS'17 Technical Program exciting.

Ka Lok Man, Tomas Krilavicius and Seungmin Rho, DATICS-IMECS'17 IPC Chairs